# Optimal Wire-Sizing Formula Under the Elmore Delay Model * 

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#### Abstract

In this paper, we consider non-uniform wire-sizing. Given a wire segment of length $\mathcal{L}$, let $f(x)$ be the width of the wire at position $x, 0 \leq x \leq \mathcal{L}$. We show that the optimal wiresizing function that minimizes the Elmore delay through the wire is $f(x)=a e^{-b x}$, where $a>0$ and $b>0$ are constants that can be computed in $O(1)$ time. In the case where lower bound $(L>0)$ and upper bound $(U>0)$ on the wire widths are given, we show that the optimal wire-sizing function $f(x)$ is a truncated version of $a e^{-b x}$ that can also be determined in $O(1)$ time. Our wire-sizing formula can be iteratively applied to optimally size the wire segments in a routing tree.


## 1 Introduction

As VLSI technology continues to scale down, interconnect delay has become the dominant factor in deep submicron designs. As a result, wire-sizing plays an important role in achieving desirable circuit performance. Recently, many wire-sizing algorithms have been reported in the literature $[1,2,4,5,7]$. All these algorithms size each wire segment uniformly, i.e., identical width at every position on the wire. In order to achieve non-uniform wire-sizing, existing algorithms have to chop wire segments into large number of small segments. Consequently, the number of variables in the optimization problem is increased substantially and thus results in long runtime and large storage.

In this paper, we consider non-uniform wire-sizing. Given a wire segment $W$ of length $\mathcal{L}$, a source with driver resistance $R_{d}$, and a sink with load capacitance $C_{L}$. For each $x \in[0, L]$, let $f(x)$ be the wire width of $W$ at position $x$. Figure 1 shows an example. Let $r_{0}$ and $c_{0}$ be the respective wire resistance and wire capacitance per unit square. Let $D$ be the Elmore delay from the source to the sink of $W$. We show that the optimal wire-sizing function $f(x)$ that minimizes $D$ satisfies an ordinary differential equation which can be analytically solved. We have $f(x)=a e^{-b x}$, where $a>0$ and $b>0$ are constants that can be computed in $O(1)$ time. These constants depend on $R_{d}, C_{L}, \mathcal{L}, r_{0}$, and $c_{0}$. In the case where lower bound ( $L>0$ ) and upper bound ( $U>0$ ) on the wire widths are given, i.e. $L \leq f(x) \leq U, 0 \leq x \leq \mathcal{L}$, we show that the optimal wire-sizing function $f(x)$ is a truncated version of $a e^{-b x}$ that can also be determined in $O(1)$ time. Our wiresizing formula can be iteratively applied to optimally size the wire segments in a routing tree.

## 2 Optimal Wire-Sizing Function

We use the Elmore delay model [3]. Suppose $W$ is partitioned into $n$ equal-length wire segments, each of length $\triangle x$

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Figure 1: Non-uniform wire-sizing.
$=\frac{\mathcal{L}}{n}$. Let $x_{i}$ be $i \triangle x, 1 \leq i \leq n$. The capacitance and resistance of wire segment $i$ can be approximated by $c_{0} \Delta x f\left(x_{i}\right)$ and $r_{0} \Delta x / f\left(x_{i}\right)$, respectively. Thus the Elmore delay through $W$ can be approximated by
$D_{n}=R_{d}\left(C_{L}+\sum_{i=1}^{n} c_{0} f\left(x_{i}\right) \Delta x\right)+\sum_{i=1}^{n} \frac{r_{0} \Delta x}{f\left(x_{i}\right)}\left(\sum_{j=i}^{n} c_{0} f\left(x_{j}\right) \Delta x+C_{L}\right)$.
The first term is the delay in the driver, which is given by the driver resistance $R_{d}$ multiplied by the total capacitance of $W$ and $C_{L}$. The second term is the sum of the delay in each wire segment $i$, which is given by its own resistance $r_{0} \triangle x / f\left(x_{i}\right)$ multiplied by its downstream capacitance $\sum_{j=i}^{n} c_{0} f\left(x_{j}\right) \triangle x+$ $C_{L}$. As $n \rightarrow \infty, D_{n} \rightarrow D$ where

$$
D=R_{d}\left(C_{L}+\int_{0}^{L} c_{0} f(x) d x\right)+\int_{0}^{L} \frac{r_{0}}{f(x)}\left(\int_{x}^{L} c_{0} f(t) d t+C_{L}\right) d x
$$

is the Elmore delay through $W$.
In this section, we derive closed-form formula for the optimal wire-sizing function $f(x)$. We consider two cases: unconstrained and constrained wire-sizing. In unconstrained wire-sizing, there is no bound on the value of $f(x)$; i.e. we determine $f:[0, \mathcal{L}] \rightarrow(0, \infty)$ that minimizes $D$. In constrained wire-sizing, we are given $L>0$ and $U<\infty$, and require that $L \leq f(x) \leq U, 0 \leq x \leq \mathcal{L}$; i.e., we determine $f:[0, \mathcal{L}] \rightarrow[L, U]$ that minimizes $D$.

### 2.1 Unconstrained Wire-Sizing

We now derive the optimal unconstrained wire-sizing function.

Theorem 1 Let $f(x)$ be an optimal wire-sizing function. We have

$$
\begin{equation*}
f^{2}(x)=\frac{r_{0}\left(C_{L}+c_{0} \int_{x}^{\mathcal{L}} f(t) d t\right)}{c_{0}\left(R_{d}+r_{0} \int_{0}^{x} \frac{1}{f(t)} d t\right)} . \tag{1}
\end{equation*}
$$

Note that $C_{L}+c_{0} \int_{x}^{\mathcal{L}} f(t) d t$ is equal to the downstream capacitance at point $x$ (denoted by $\Gamma_{x}$ ) and $R_{d}+r_{0} \int_{0}^{x} \frac{1}{f(t)} d t$ is
equal to the upstream resistance at point $x$ (denoted by $\Phi_{x}$ ). Hence we can rewrite Equation (1) as follows:

$$
\begin{equation*}
f(x)=\sqrt{\frac{r_{0} \Gamma_{x}}{c_{0} \Phi_{x}}} . \tag{2}
\end{equation*}
$$

By rearranging the terms in (1) and differentiating it with respect to $x$ twice, we get the following theorem.

Theorem 2 Let $f(x)$ be an optimal wire-sizing function. We have

$$
\begin{equation*}
f^{\prime \prime}(x) f(x)=f^{\prime}(x)^{2} \tag{3}
\end{equation*}
$$

The following theorem shows that the differential equation in (3) has a closed-form solution.

Theorem 3 Let $f(x)=a e^{-b x}$, where $a=\frac{r_{0}}{b R_{d}}$ and

$$
\begin{equation*}
b \sqrt{\frac{R_{d} C_{L}}{r_{0} c_{0}}}-e^{\frac{-b L}{2}}=0 \tag{4}
\end{equation*}
$$

We have, $f(x)$ is an optimal wire-sizing function.
Note that the function $g(z)=z \sqrt{\frac{R_{d} C_{L}}{r_{0} C_{0}}}-e^{\frac{-z L}{2}}$ is a strictly increasing function in $z, g(0)<0$, and $\lim _{z \rightarrow \infty} g(z)>0$. Thus $g(z)$ has a unique root $b>0$. We can use Newton-Raphson method to determine $b$ and, in practice, five to seven iterations are sufficient. Since $a=\frac{r_{0}}{R_{d} b}$ and $b>0, a>0$. Figure 2 shows the exponentially decreasing nature of the optimal wire-sizing function.


Figure 2: Optimal unconstrained wire-sizing.

### 2.2 Constrained Wire-Sizing



Figure 3: Direct truncation according to $U$ and $L$ is not optimal.

We now consider constrained wire-sizing. It is clear that if the wire-sizing function $f(x)$ obtained for the unconstrained case lies within bounds $L$ and $U$, then $f(x)$ is also optimal for constrained wire sizing. On the other hand, if for some $x, f(x)$ is not in $[L, U]$, a simple approach is to round $f(x)$ to either $L$ or $U$; i.e. the new function is obtained by a direct truncation
of $f(x)$ by $y=L$ and $y=U$. (See Figure 3.) Unfortunately, the resulting function is not optimal. The reason is that, when we consider the point $v_{1}$, which is the intersection of the curves $f(x)=a e^{-b x}$ and $y=U$, from Equation (2) $v_{1}$ must satisfy

$$
\begin{equation*}
f\left(v_{1}\right)=\sqrt{\frac{r_{0} \Gamma_{v_{1}}}{c_{0} \Phi_{v_{1}}}}, \tag{5}
\end{equation*}
$$

for $v_{1}$ to be on the optimal curve. However, from Figure 3, it is clear that $v_{1}$ does not satisfy Equation (5), because both of its upstream resistance and downstream capacitance should be recalculated according to the new function, in which the two values associated with $v_{1}$ are reduced because of the truncation. Thus this simple approach is not optimal.

Note that the optimal unconstrained wire-sizing function is a decreasing function. We can show that the optimal constrained wire-sizing function must also be decreasing.

Theorem 4 Let $f(x)$ be an optimal constrained wire-sizing function. We have, $f(x)$ is decreasing on $[0, L]$.

According to Theorem 4, the optimal wire-sizing function $f(x)$, similar to the one shown in Figure 3, consists of (at most) three parts. The first part is $f(x)=U$, the middle part is a decreasing function, and the last part is $f(x)=L$. The three parts of $f(x)$ partition $W$ into three wire segments, $A$, $B$, and $C$, where $A$ has width $U, C$ has width $L$, and $B$ is defined by the middle part of $f(x)$. It is easy to see that the middle part of $f(x)$ must be of the form $f(x)=a e^{-b x}$ for some $a>0$ and $b>0$. To see this, we can consider the wire segment $A$ to be a part of the driver and its resistance to be a part of $R_{d}$. Similarly, the wire segment $C$ can be considered as a part of the load and its capacitance as a part of $C_{L}$. According to Equation (4), we can recalculate $a$ and $b$ using the new values of $R_{d}$ and $C_{L}$, as long as we know the length of the wire segments $A$ and $B$.

As mentioned before, not all three parts of $f(x)$ needed to be present. In fact, an optimal constrained wire-sizing function $f(x)$ can be of any one of the six types of functions (type-A, type-B, type-C, type- AB , type- BC , and type- ABC ) as shown in Figure 4. Note that the six function types clearly are named after the wire-segment types which are presented in $W$. For example, in a type- $A B$ function, $W$ consists only of wire segments A and B. As shown in Figure $4, l_{1}, l_{2}$, and $l_{3}$ are the length of wire segments $\mathrm{A}, \mathrm{B}$, and C , respectively.

We now define six wire-sizing functions $f_{A}, f_{B}, f_{C}, f_{A B}$, $f_{B C}, f_{A B C}$ as follows: All six functions are of the form

$$
f(x)= \begin{cases}U & , 0 \leq x \leq l_{1} \\ a e^{-b x} & , l_{1} \leq x \leq l_{1}+l_{2} \\ L & , l_{1}+l_{2} \leq x \leq l_{1}+l_{2}+l_{3}=\mathcal{L}\end{cases}
$$

where the parameters $a, b, l_{1}, l_{2}$, and $l_{3}$ for the six functions are given in Table 1. Typically, the names of the functions correspond to their types, i.e., $f_{A}$ is of type-A, $f_{B}$ is of type$B$, and so on, but it is not always true. For example, it is possible that after we compute the parameters for $f_{A B}$ we get $l_{1} \geq \mathcal{L}$ and hence it is of type- A ; it is also possible that $f_{A B}$ degenerates into a type- $B$ function. In this case, we say that $f_{A B}$ is of the wrong type. We also note that sometimes the functions may be illegal in the sense that they violate the

|  | $l_{1}$ | $l_{2}$ | $l_{3}$ | $a$ | $b$ |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $f_{A}$ | $\mathcal{L}$ | 0 | 0 | U | 0 |
| $f_{B}$ | 0 | $\mathcal{L}$ | 0 | $\Psi_{1}(a)=0$ | $\frac{r_{0}}{a R_{d}}$ |
| $f_{C}$ | 0 | 0 | $\mathcal{L}$ | $L$ | 0 |
| $f_{A B}$ | $\Psi_{2}\left(l_{1}\right)=0$ | $\mathcal{L}-l_{1}$ | 0 | $U$ | $\frac{r_{0}}{R_{d} U+r_{0} l_{1}}$ |
| $f_{B C}$ | 0 | $\mathcal{L}-l_{3}$ | $\Psi_{3}\left(l_{3}\right)=0$ | $\frac{r_{0}\left(C_{L}+c_{0} L l_{3}\right)}{R_{d} c_{0} L}$ | $\frac{c_{0} L}{c_{L}+c_{0} L l_{3}}$ |
| $f_{A B C}$ | $\frac{\frac{C_{L}}{C_{0} L}+\mathcal{L}-\left(1+\ln \frac{U}{L}\right) \frac{U R_{d}}{r_{0}}}{2+\ln \frac{U}{L}}$ | $\frac{\ln \frac{U}{L}\left(\frac{C_{L}}{c_{0} L}+\mathcal{C}+\frac{U R_{d}}{r_{0}}\right)}{2+\ln \frac{U}{L}}$ | $\mathcal{L}-l_{2}-l_{1}$ | $U$ | $\frac{r_{0}}{R_{d} U+r_{0} l_{1}}$ |
| $\Psi_{1}(a)=a^{2}-\frac{R_{0} C_{L}}{c_{0} R_{d}} e^{\frac{r_{0} L}{a R_{d}}} ; \Psi_{2}\left(l_{1}\right)=\frac{r_{0} \mathcal{L}+R_{d} U}{R_{d} U+r_{0} l_{1}}-\ln \frac{c_{0} U\left(R_{d} U+r_{0} l_{1}\right)}{r_{0} C_{L}}-1 ; \Psi_{3}\left(l_{3}\right)=\frac{C_{L}+c_{0} L \mathcal{L}}{C_{L}+c_{0} L l_{3}}-\ln \frac{r_{0}\left(C_{L}+c_{0} L l_{3}\right)}{c_{0} R_{d} L^{2}}-1 .$ |  |  |  |  |  |

Table 1: Definitions of the wire-sizing functions $f_{A}, f_{B}, f_{C}, f_{A B}, f_{B C}$, and $f_{A B C}$.


Figure 4: Six types of optimal wire-sizing functions.
wire-width constraints. Nevertheless, we can show that these six functions are candidates for an optimal constrained wiresizing function $f(x)$. In fact, if we eliminate the functions that are either illegal or of the wrong type, an optimal wire-sizing function can be chosen as the best one (in terms of delay) among the remaining ones. We have the following theorem.

Theorem 5 Let $G \subseteq F=\left\{f_{A}, f_{B}, f_{C}, f_{A B}, f_{B C}, f_{A B C}\right\}$ be the set of functions that are either illegal or of the wrong type. Let $f \in F-G$ be a function which has minimum delay. We have, $f$ is an optimal constrained wire-sizing function.

The above method always requires the computation of all six functions in $F$. With the help of additional analysis, we can speed up the procedure. Table 2 shows a set of six feasibility conditions $\left\{\varphi_{A}, \varphi_{B}, \varphi_{C}, \varphi_{A B}, \varphi_{B C}, \varphi_{A B C}\right\}$ on $\mathcal{L}$. Let $\Gamma=\{A, B, C, A B, C B, A B C\}$.

## Lemma 1 The six feasibility conditions

$\left\{\varphi_{A}, \varphi_{B}, \varphi_{C}, \varphi_{A B}, \varphi_{B C}, \varphi_{A B C}\right\}$ cover all possible $\mathcal{L}>0$.

Moreover, if $\mathcal{L}$ satisfies $\varphi_{z}$, where $z \in \Gamma$, then $f_{z}$ is legal and is of type-z.

Theorem 6 Let $H=\left\{f_{z} \mid z \in \Gamma\right.$ and $\mathcal{L}$ satisfies $\left.\varphi_{z}\right\}$. Let $f \in H$ be a function which has minimum delay. We have, $f$ is an optimal constrained wire-sizing function.

According to Theorem 6, we only need to check the six feasibility conditions. Only the functions in $H$ needed to be computed. In general, $|H|<6$; in fact, we have never encountered any case where $|H|>1$.

| $\varphi_{A}$ | $\mathcal{L} \leq \frac{C_{L}}{c_{0} U}-\frac{R_{d} U}{r_{0}}$ |
| :---: | :--- |
| $\varphi_{B}$ | $\mathcal{L} \leq \min \left\{\frac{U R_{d}}{r_{0}} \ln \frac{c_{0} R_{d} U^{2}}{r_{0} C_{L}}, \frac{C_{L}}{r_{0} L} \ln \frac{r_{0} C_{L}}{c_{0} R_{d} L^{2}}\right\}$ |
| $\varphi_{C}$ | $\mathcal{L} \leq \frac{R_{d} L}{r_{0}}-\frac{C_{L}}{c_{0} L}$ |
| $\varphi_{A B}$ | $\mathcal{L}>\max \left\{\frac{U R_{d}}{r_{0}} \ln \frac{c_{0} R_{d} U^{2}}{r_{0} C_{L}}, \frac{C_{L}}{c_{0} U}-\frac{R_{d} U}{r_{0}}\right\}$ and |
|  | $\mathcal{L} \leq\left(1+\ln \frac{U}{L}\right) \frac{C_{L} L}{c_{0} L}-\frac{R_{d} U}{r_{0}}$ |$|$| $\varphi_{B C}$ |
| :---: |
|  |
| $\varphi_{A B C}$ |

Table 2: Feasibility Conditions.


Figure 5: Relationships among the six types of functions with respect to $\mathcal{L}$ and $\frac{R_{d}}{C_{L}}$.

We also have the following interesting observations. In Figure 5, we show the relationships among the six types of optimal wire-sizing functions with respect to the three parameters:
wire length $\mathcal{L}$, driver resistance $R_{d}$, and load capacitance $C_{L}$. The horizontal axis represents the ratio of the driver resistance to the load capacitance. The vertical axis represents the wire length $\mathcal{L}$. Suppose we keep $\frac{R_{d}}{C_{L}}$ fixed and varies $\mathcal{L}$. When $\mathcal{L}$ is small, optimal wire-sizing functions tend to be of type-A, type-B, or type-C. As we increase the $\mathcal{L}$, wire-sizing function types will change to type- AB or type- BC when $\mathcal{L}$ is of moderate size and will be of type- ABC when $\mathcal{L}$ is large. Suppose we keep $\mathcal{L}$ fixed and varies $\frac{R_{d}}{C_{L}}$. When $\mathcal{L}$ is small, as we increase $\frac{R_{d}}{C_{L}}$, optimal wire-sizing function will change from type-A to type-B and then to type-C. When $\mathcal{L}$ is of moderate size, optimal wire-sizing function will change from type- AB to type- BC as we increase $\frac{R_{d}}{C_{L}}$. Roughly speaking, the larger the ratio $\frac{R_{d}}{C}$, the smaller the wire sizes. When the wire length $\mathcal{L}$ is very large, optimal wire-sizing function is most likely to be of type-ABC.

## 3 Application to Routing Trees

Our wire-sizing formula can be applied to size a general routing tree. Recently, [2] presents a wire-sizing algorithm GWSA-C for sizing the wire segments in routing trees. Each segment in the tree is sized uniformly, i.e. uniform wire width per segment. Basically, GWSA-C is an iterative algorithm with guaranteed convergence to a global optimal solution. In each iteration of GWSA-C, the wire segments are examined one at a time; each time a wire segment is uniformly re-sized optimally while keeping the widths of the other segments fixed. We can incorporate our wire-sizing formula into GWSA-C to size each wire segment non-uniformly. When we apply our wire-sizing formula to size a wire segment in a tree, $R_{d}$ should be set to be the total upstream resistance including the driving resistance, and the $C_{L}$ should be set to be the total downstream capacitance, including the load capacitances of the sinks in the subtree. It can be shown that this modified algorithm always converges to a global optimal solution.

## 4 Experimental Results

We implemented and tested our algorithm in C on a Sun Sparc 5 workstation with 16 MB memory. The parameters used are shown in Table 3. The results are given in Table 4. The first column labeled "Precision Requirement" specifies the required accuracy of the wire width values. The second column shows the number of Newton-Raphson iterations. Since the wire-sizing formula can be derived in $O(1)$ time, we are interested in knowing the cost of the numerical method adopted to obtain some parameter values. It shows that even under very strict precision requirement the number of iterations is at most 7 . Our method is extremely fast.

We also performed experiments to compare the nonuniform wire-sizing solutions with the uniform ones in which wires are chopped into different number of segments. The results are drawn in Figure 6. Wire widths are plotted as the functions of positions on the wire segments. It shows that the more segments a wire is chopped into, the closer the solution is to our formula. When the wire is chopped into 1000 seg ments, it can be shown that the corresponding curve and the non-uniform wire-size curve are almost identical.

## References

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| Unit Resistance: | 0.008 | $\Omega / \mu \mathrm{m}$ |
| :---: | ---: | ---: |
| Unit Capacitance: | $6 E-17$ | $F / \mu \mathrm{m}$ |
| Minimum Wire Width: | 1 | $\mu \mathrm{~m}$ |
| Maximum Wire Width: | 3.5 | $\mu \mathrm{~m}$ |
| Driver Resistance: | 25 | $\Omega$ |
| Load Capacitance: | $1 E-12$ | $F$ |

Table 3: RC Parameters

| Precision Requirement $(\mu \mathrm{m})$ | \# of iterations |
| :---: | :---: |
| 0.1 | 5 |
| 0.01 | 5 |
| 0.001 | 5 |
| 0.0001 | 6 |
| 0.00001 | 6 |
| 0.000001 | 7 |

Table 4: The number of Newton-Raphson iterations.

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Figure 6: Approximating non-uniform wire-sizing by uniform wire-sizing.


[^0]:    * This work was partially supported by the Texas Advanced Research Program under Grant No. 003658459.

