

TLM-ADI Simulator

--- A Linear-Time Power Grid Transient Simulator

Yu-Min Lee, and Charlie Chung-Ping Chen

Electrical and Computer Engineering, University of Wisconsin-Madison

Characteristics of the TLM-ADI Simulator

- A Transient Power Grid Simulator
- Linear Runtime and Memory requirement
- Unconditionally Stable
- Powered by Transmission-Line-Modeling Alternating-Direction-Implicit (TLM-ADI) Method for Fast Simulation
- Deal with Inhomogeneous Cases.

Motivation

Trend of VLSI Technology

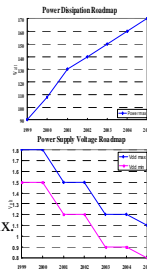
- Increasing power dissipation
- Decreasing supply voltage

Power Fluctuation Sources Increase Significantly

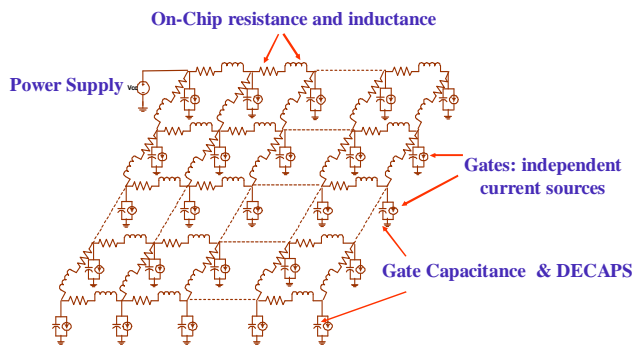
- IR-drop: $\Delta V = I \times R = (P / V_{dd}) \times R$
- L di/dt: $\Delta V = L \, di/dt \sim L (P / V_{dd}) \times f$
- Other noises such as resonance and electromigration

Power Grids Analysis Challenges

- More than 40-million transistors on a chip
- Sparse direct method takes super linear time to solve a matrix.



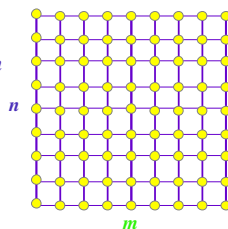
Power Grid Modeling



Key Idea: ADI Method

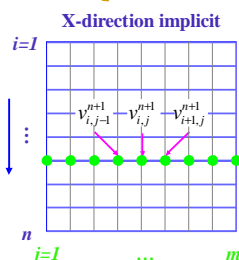
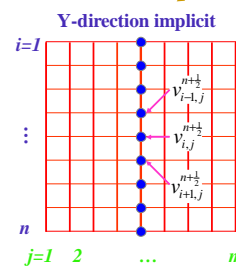
Total Runtime: $O(2mn) = O(N)$

Total nodes: $N = mn$



Step I

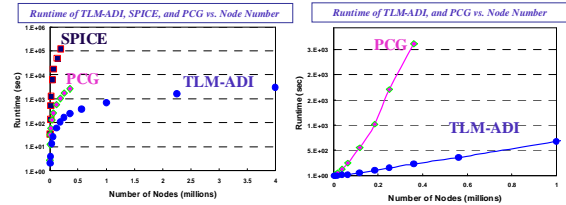
Step II



Runtime for solving each x: $O(n)$

Runtime for solving each y: $O(m)$

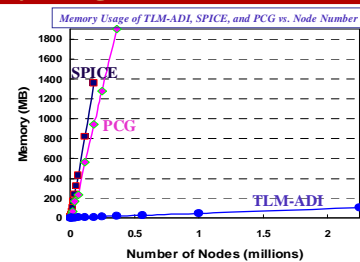
Linear Runtime



TLM-ADI is over 1000 times faster than SPICE, and 10 times faster than PCG

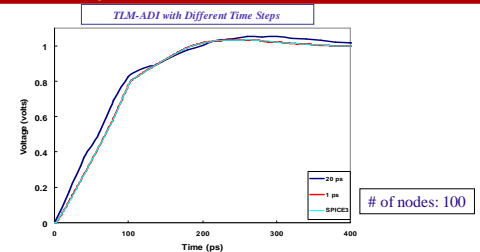
- Runtime Comparison based on the TLM-ADI, SPICE, and Preconditioned Conjugate Gradient (PCG)
- The runtime of the TLM-ADI Simulator is linearly proportional to the number of the discretization nodes.

Memory Usage



- Memory Usage Comparison based on the TLM-ADI, SPICE, and PCG.
- The memory usage of the TLM-ADI Simulator is linearly proportional to the number of nodes.

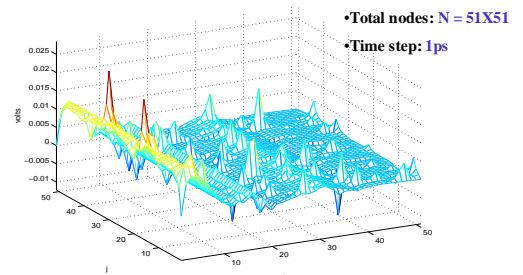
Unconditionally Stable



The Courant stability constraint is 1.9442 ps

- The time step of TLM-ADI is not limited by any stability constraint

A Snapshot of Transient Response



Contact Information

University of Wisconsin-Madison VLSI-EDA Group
<http://vlsi.ece.wisc.edu>

Professor: Charlie Chung-Ping Chen
chen@engr.wisc.edu

Developed by: Yu-min Lee
yu-min@cae.wisc.edu

Download TLM-ADI
<http://vlsi.ece.wisc.edu/TLM-ADI.htm>